

Applicant	James D. Beasom	<b><u>PRELIMINARY</u></b> <b><u>AMENDMENT</u></b>
Serial No.	Unknown	
Filing Date	Even date herewith	
Group Art Unit	Unknown	
Examiner Name	Unknown	
Attorney Docket No.	125.008US02	
Title: INTEGRATED CIRCUIT WITH A MOS STRUCTURE HAVING REDUCED PARASITIC BIPOLAR TRANSISTOR ACTION		

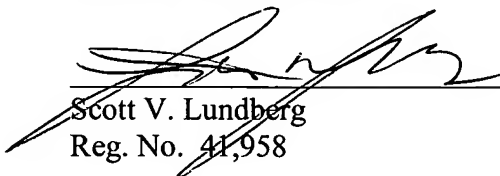
MAIL STOP PATENT APPLICATION  
 Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

**IN THE CLAIMS**

Please note that pending claims 1-54 are non-elected claims 28-76 and 86-91 from the priority application U.S. Serial No. 09/977,188. (Attorney Docket No. 125.008US01).

Respectfully submitted,

Date: 3-26-04

  
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 Reg. No. 41,958

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